

Claims

- [c1] 1. An enclosure services processor card arranged to selectively split a fibre-channel arbitrated-loop (FC-AL) into two split loops, said card being adapted to plug into a backplane for a rack enclosure and including a first switch operatively connected to a hub for said FC-AL, said hub comprising a plurality of port bypass circuits, each port bypass circuit being connected to a pair of tracks which in use connect to a respective one of each of said devices comprising said fibre channel arbitrated loop, said hub further comprising a pair of switches operatively controlled by said first switch, said pair of switches being disposed between respective port bypass circuits at which said loop is to be split so that in a first state said pair of switches connect said devices in a single loop and in a second state said pair of switches divide said devices into two split loops.
- [c2] 2. An enclosure services card as claimed in claim 1 wherein said card includes an enclosure services processor operatively connected to said first switch, said enclosure services processor being operable to selectively control said first switch to split said fibre-channel devices into two split loops.
- [c3] 3. An enclosure services card as claimed in claim 1 wherein said first switch comprises a jumper, said jumper being configurable to selectively split said fibre-channel devices into two split loops.
- [c4] 4. An enclosure services card as claimed in claim 1 wherein said hub further comprises a further port bypass circuit being connected to a pair of tracks which in use connect to an expansion port through which a further one or more devices can be connected to said loop.
- [c5] 5. An enclosure services card as claimed in claim 4 wherein said hub further comprises a pair of tracks connected between a pair of port bypass circuits and which in use connect to a host device.
- [c6] 6. An enclosure services card as claimed in claim 5 wherein said pair of tracks are arranged to locate said host device in a first split loop and said further port

bypass circuit is arranged to locate said expansion port in a second split loop.

- [c7] 7. A rack enclosure comprising a processor card as claimed in claim 1, a backplane and a plurality of devices connected to said backplane forming one or more fibre channel arbitrated loops.

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